

FIG. 2

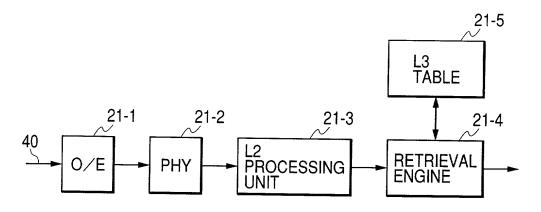


FIG. 3

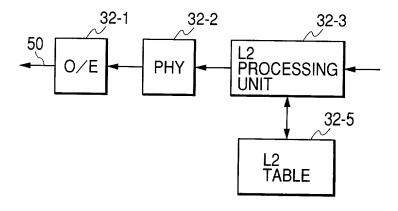


FIG. 4

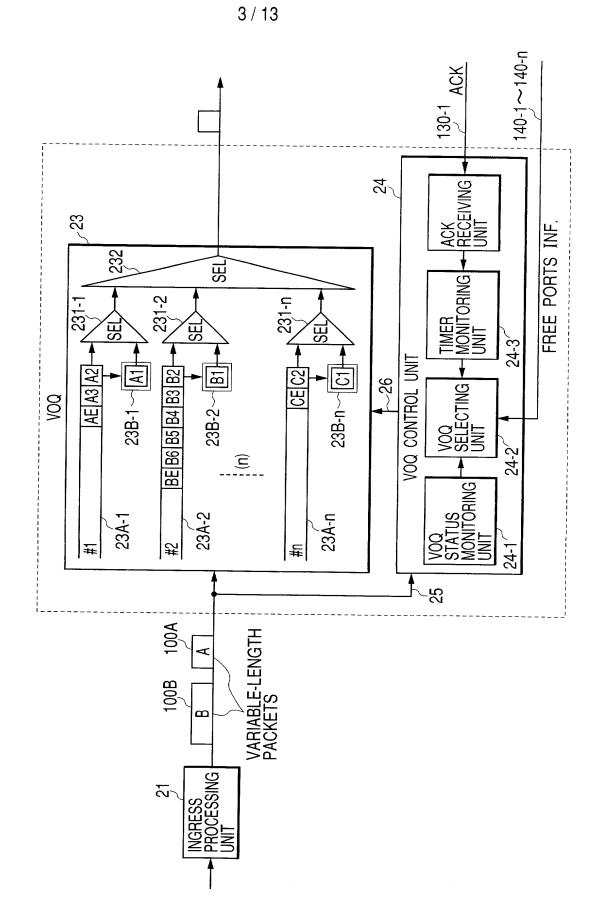


FIG. 5

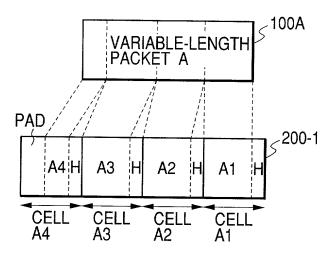
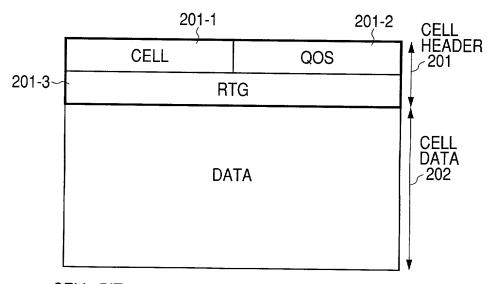


FIG. 6

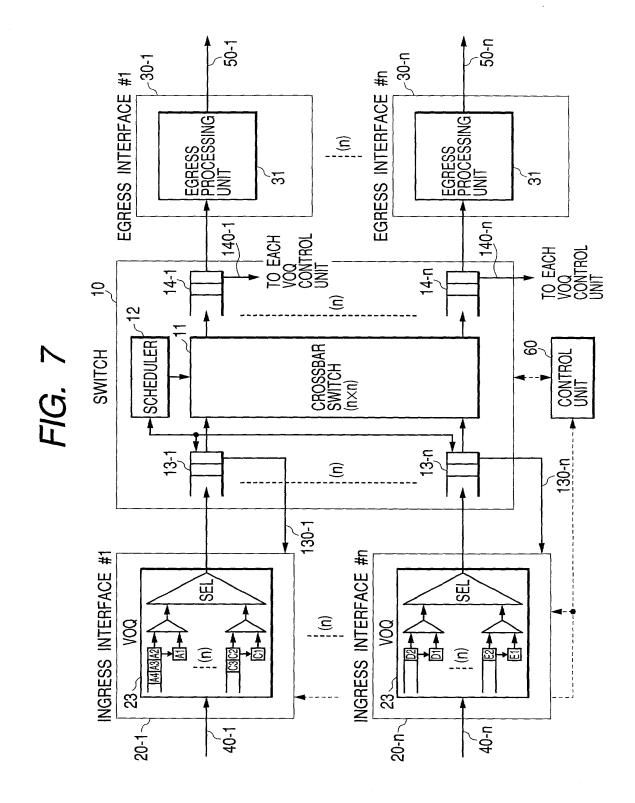


**CELL BIT** 

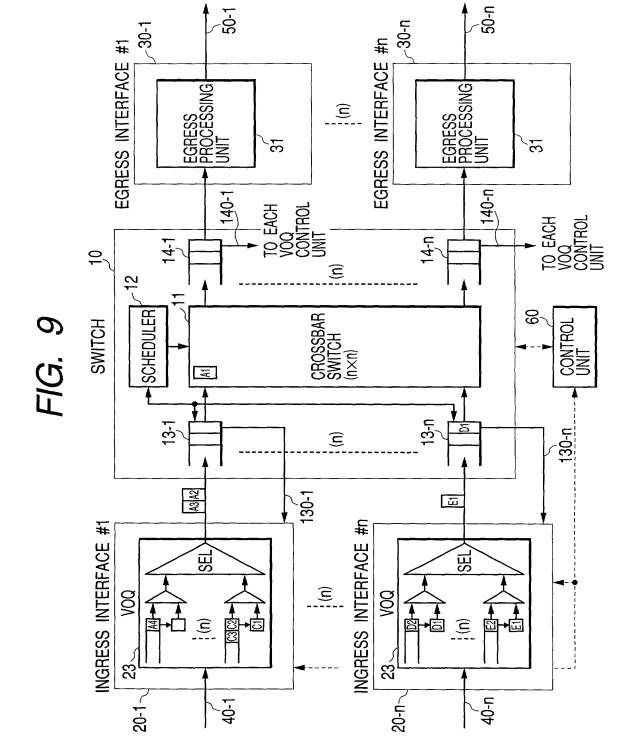
0xx: INVALID CELL 110: FIRST CELL

100: INTERMEDIATE CELL

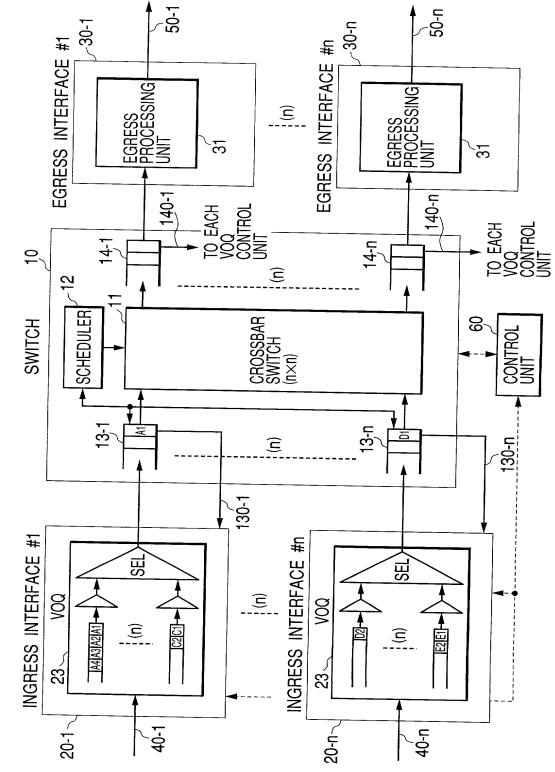
101: LAST CELL



50-n 50-1 \_30-n -30-1 EGRESS INTERFACE #n EGRESS INTERFACE #1 EGRESS PROCESSING-UNIT Ê ઝ 3 140-1 140-n TO EACH VOO CONTROL UNIT 14-n 14-1 9  $\widehat{\boldsymbol{\Xi}}$ 75 CROSSBAR SWITCH (n×n) SCHEDULER F1G. 8 CONTROL SWITCH 130-n  $\Xi$ 130-1 INGRESS INTERFACE #n INGRESS INTERFACE #1 SEL SEL Ē 8 Š Ê Ê Ŕ 20-n 40-n 40-1









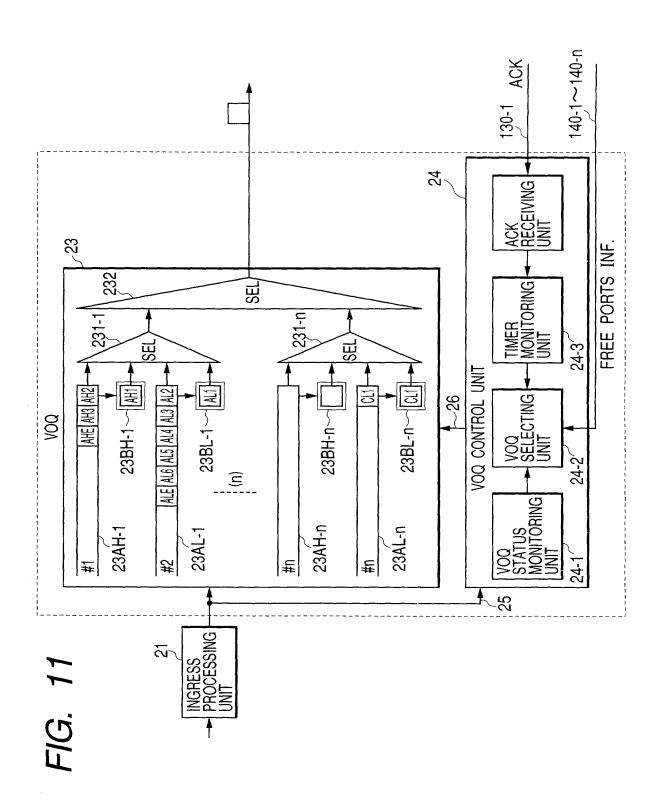


FIG. 12

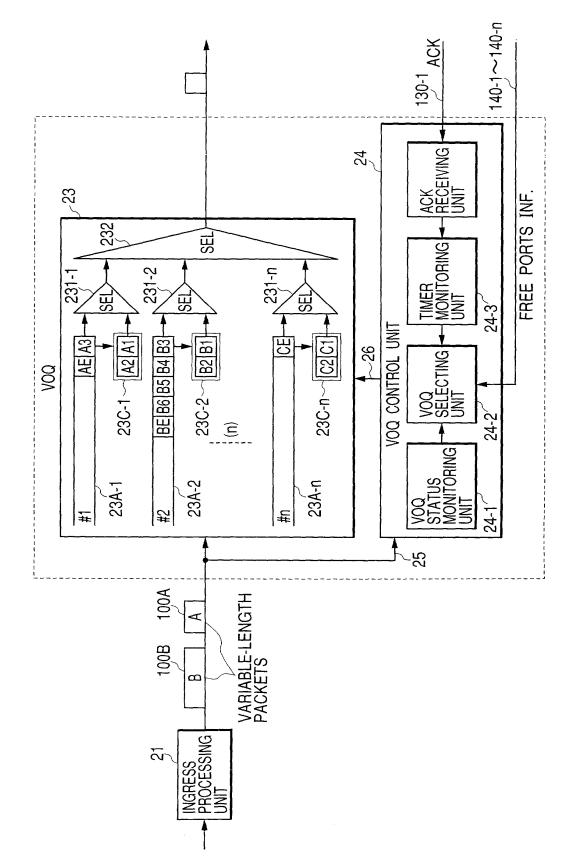


FIG. 13

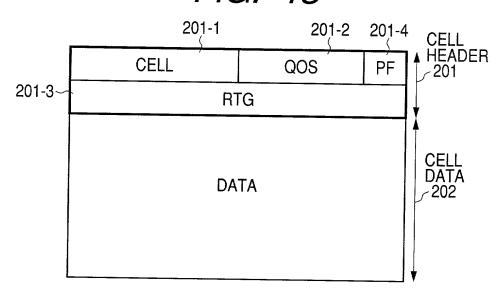


FIG. 14

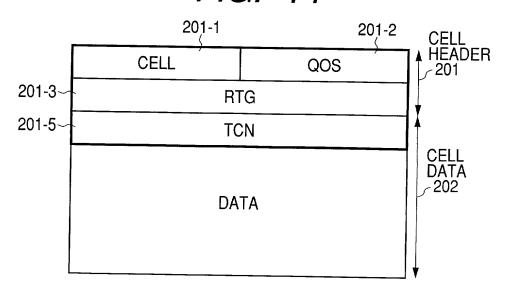
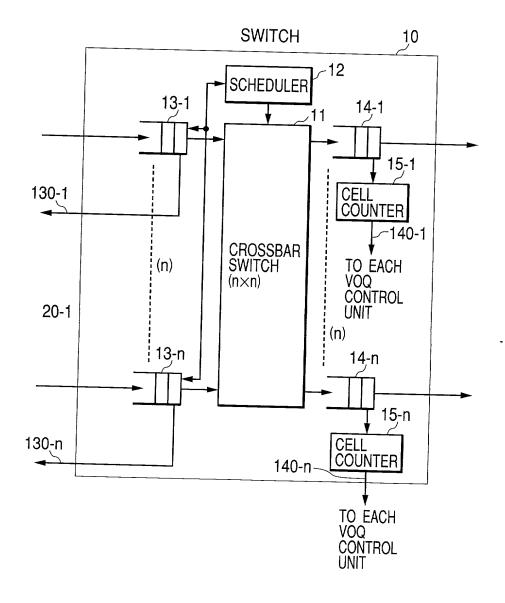


FIG. 15



## FIG. 16A

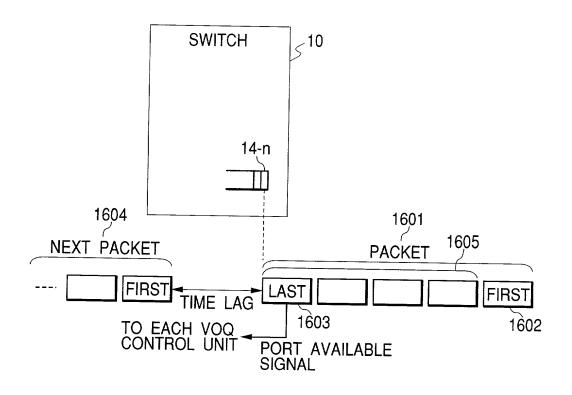


FIG. 16B

